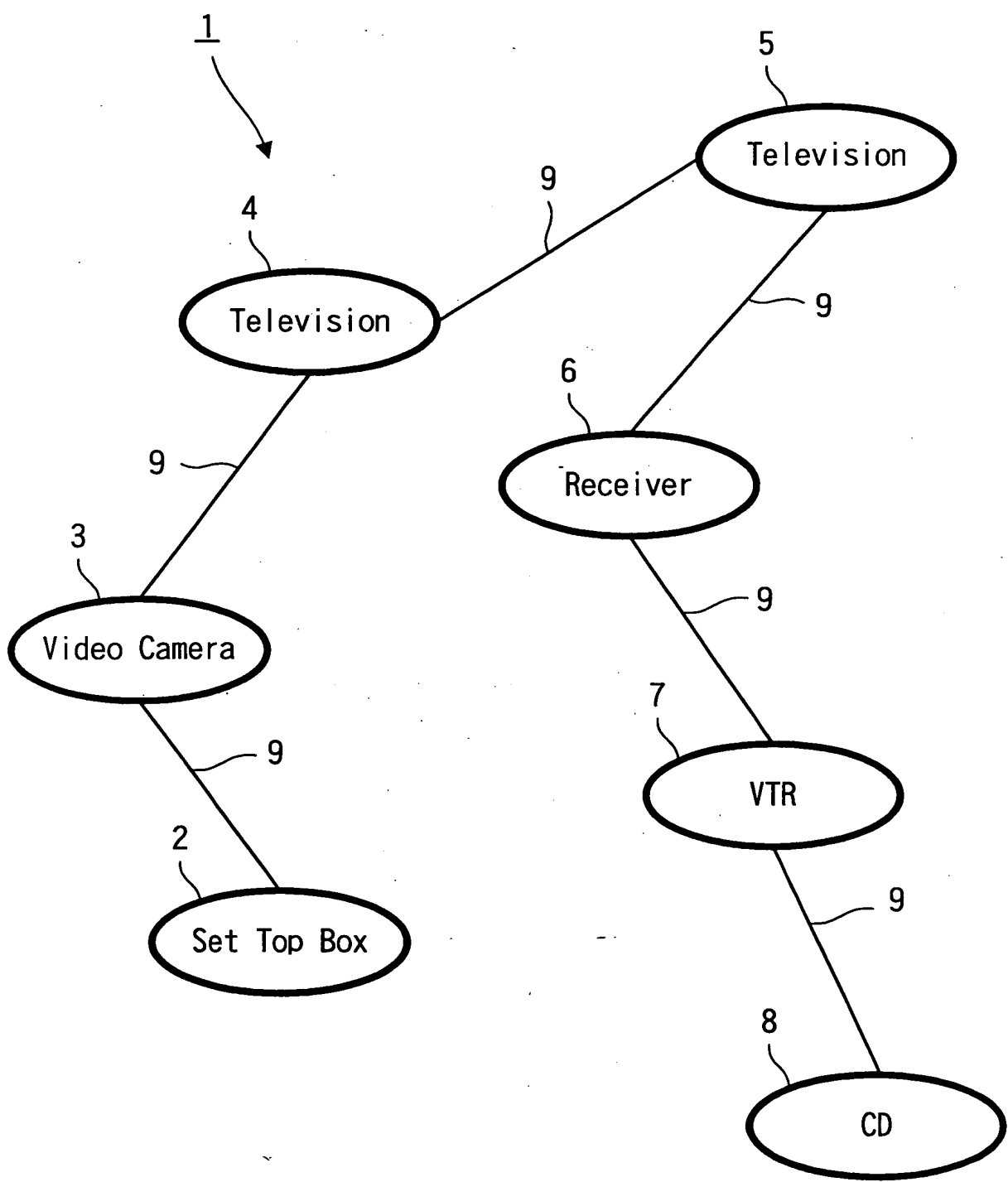


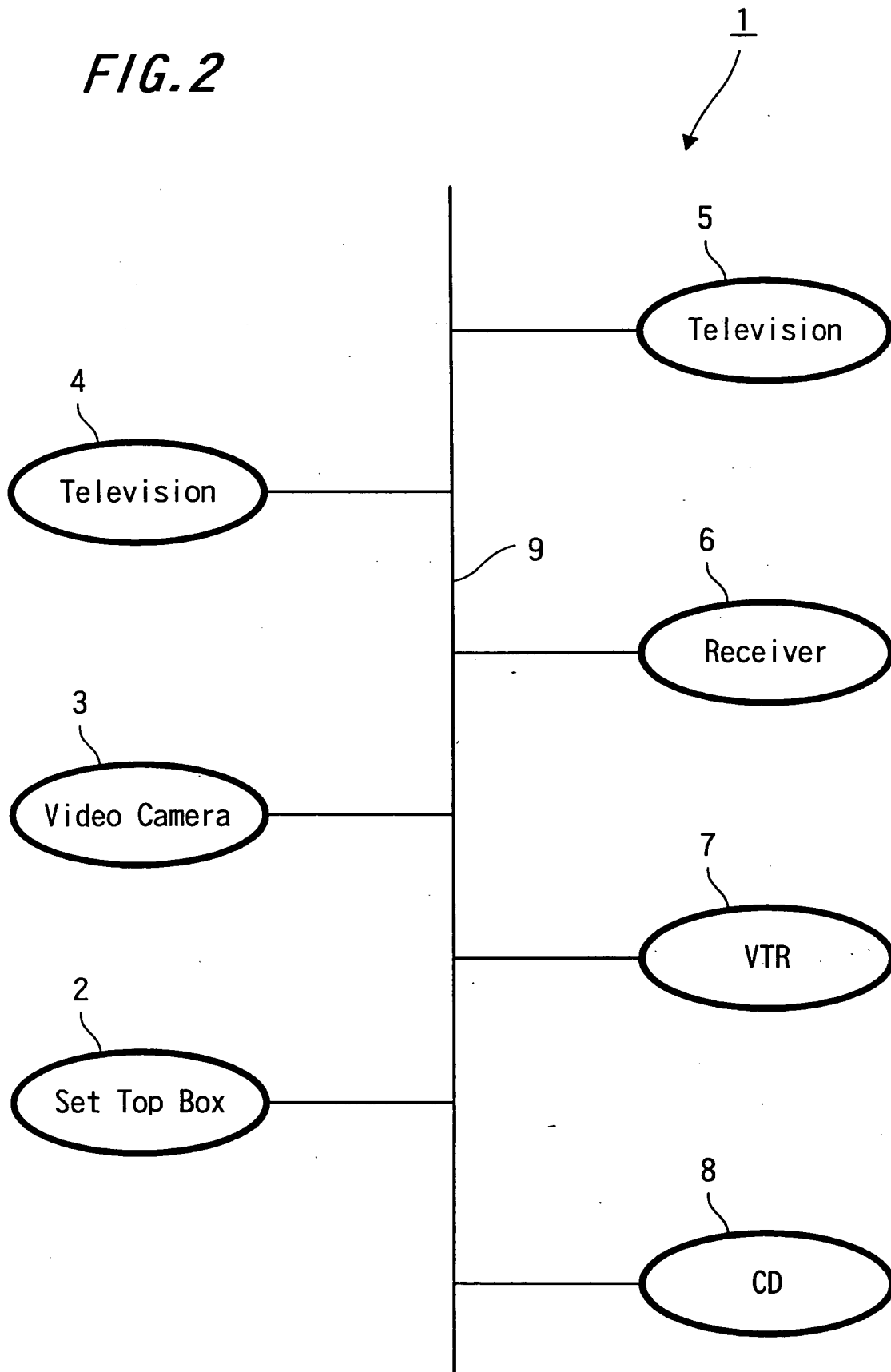
FIG. 1



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*FIG. 2*



**FIG. 3**

The diagram illustrates a television system architecture. A central 'Set Top Box' (2) is connected to a 'Video Camera' (3) via a bidirectional arrow. It also has bidirectional connections to two 'Television' units (4 and 5). Unidirectional arrows point from the 'Set Top Box' to a 'Receiver' (6), a 'VTR' (7), and a 'CD' (8). A unidirectional arrow points from the 'VTR' (7) to the 'Receiver' (6). A curved arrow labeled '9' indicates a signal path from the 'Set Top Box' area towards the 'VTR' and 'Receiver'.

**FIG. 4**

The diagram illustrates a system architecture enclosed in a large oval labeled **2**. Inside the oval, the following components are shown:

- Processor** (13)
- ROM** (15)
- RAM** (14)
- Mass Storage Unit** (16)
- Video /Audio Receiver** (10)
- MPEG Unit** (11)

A horizontal bus line connects the Processor (13), ROM (15), RAM (14), and Mass Storage Unit (16). The Mass Storage Unit (16) is also connected to the MPEG Unit (11). The MPEG Unit (11) is connected to the Video /Audio Receiver (10). A **Bus Interface** (17) is connected to the horizontal bus line and to an external bus labeled **9**.



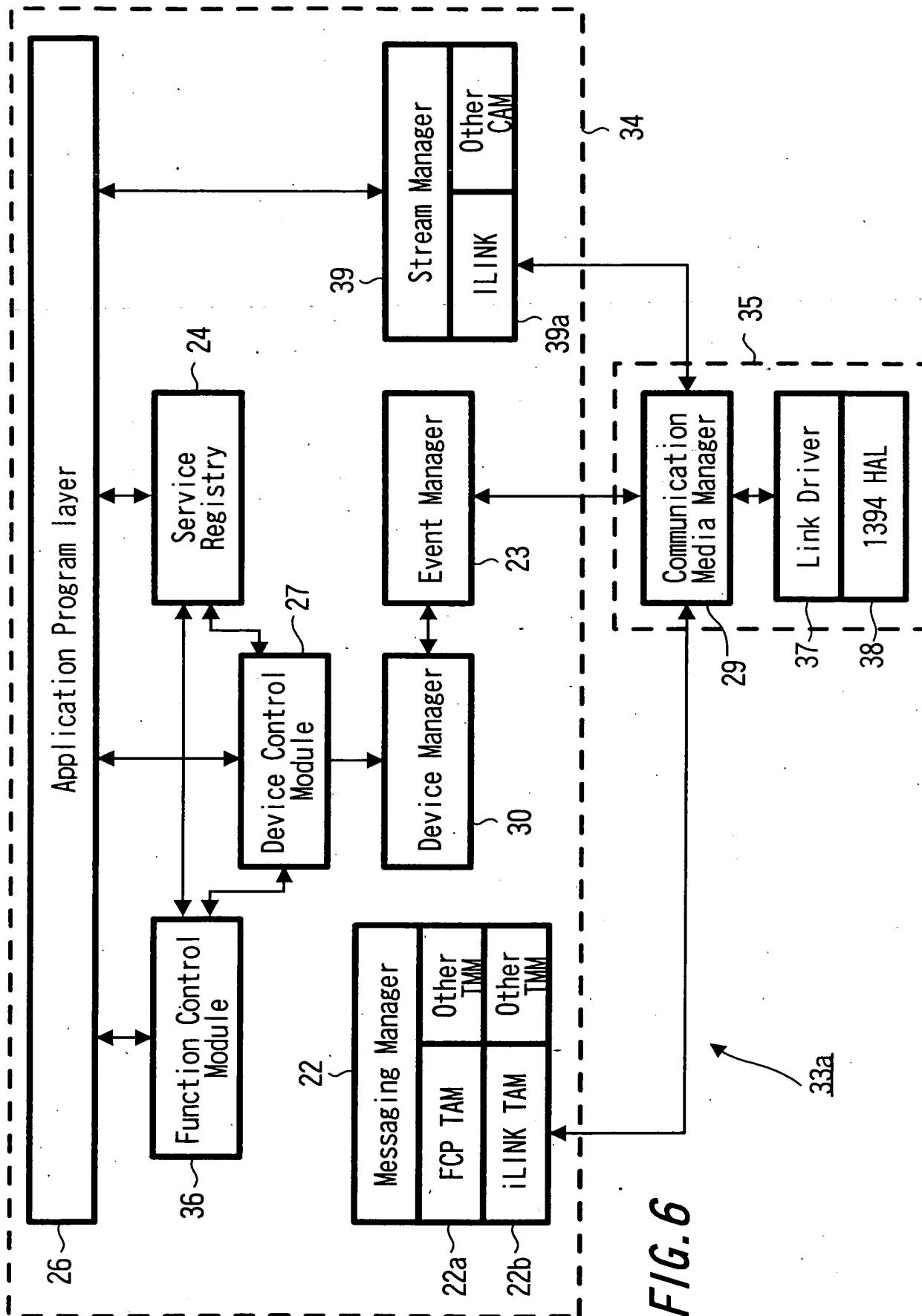


FIG. 7

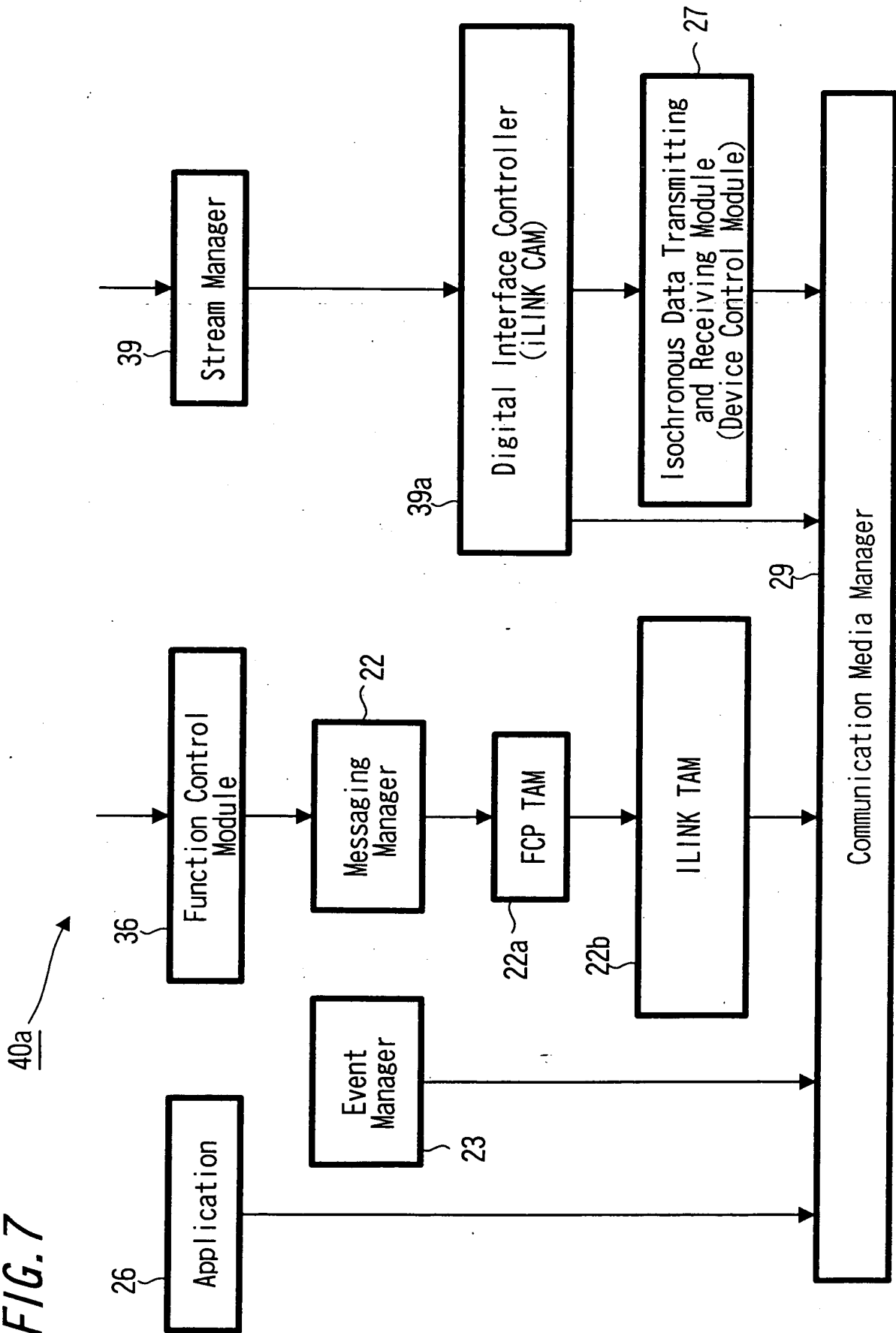


FIG. 8

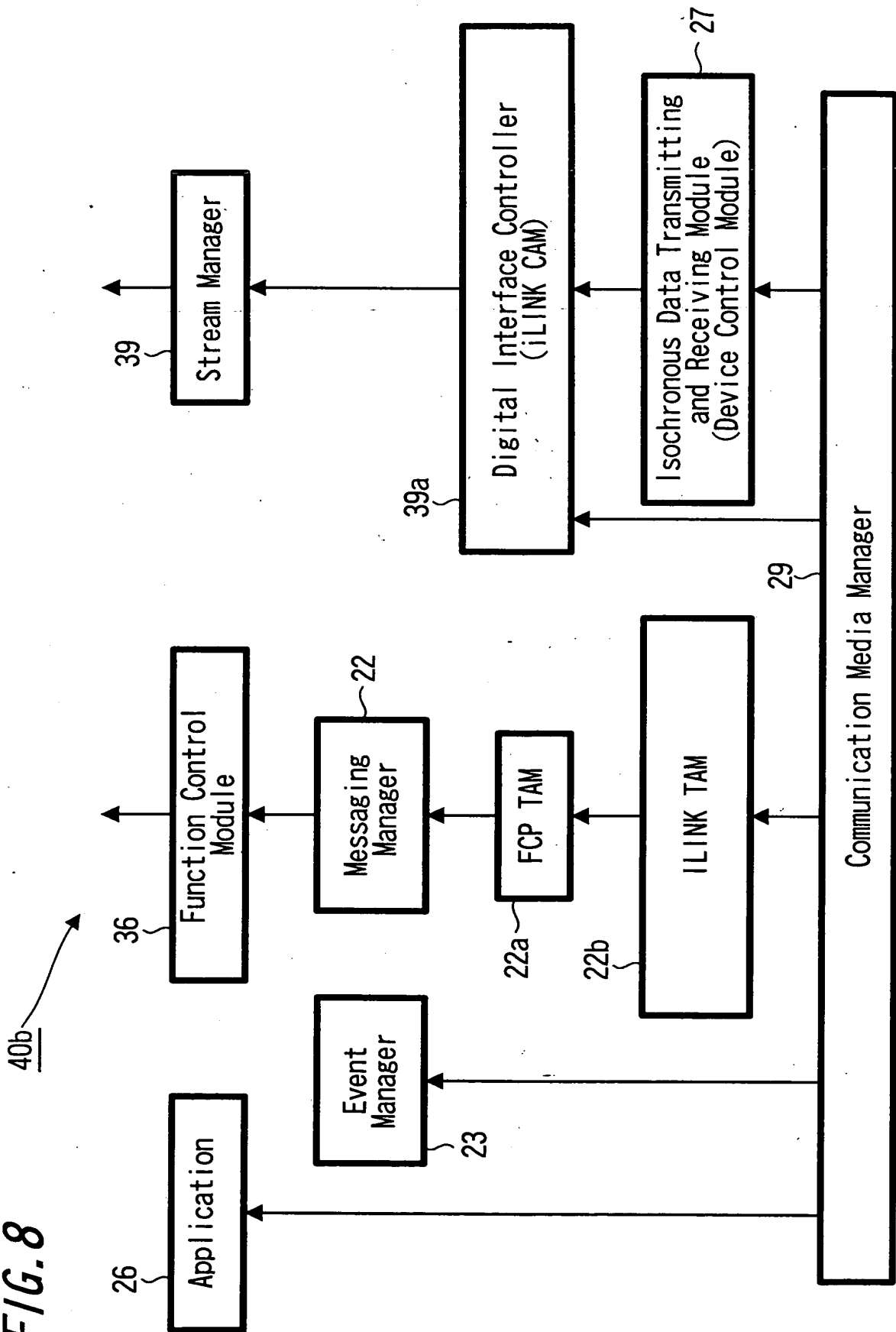


FIG. 9

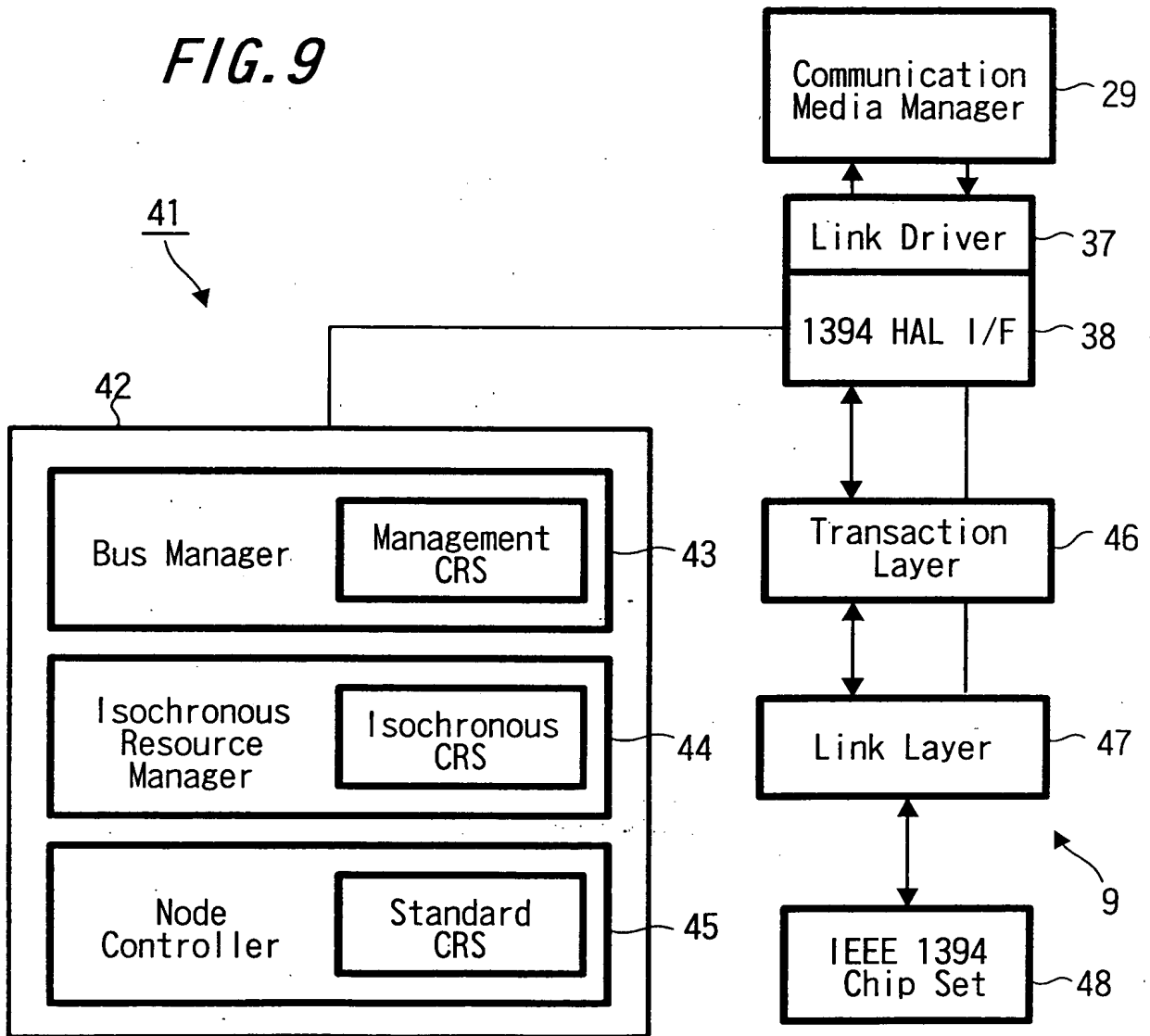




FIG. 10

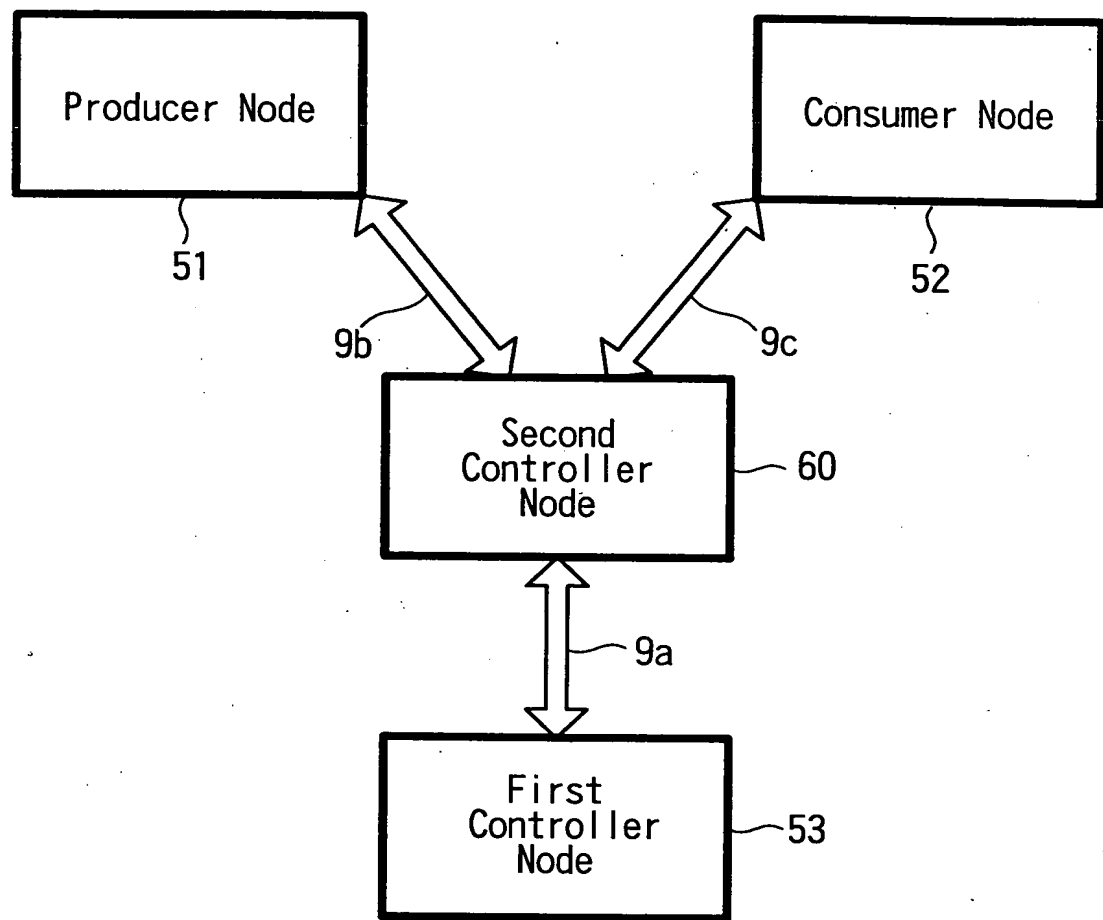
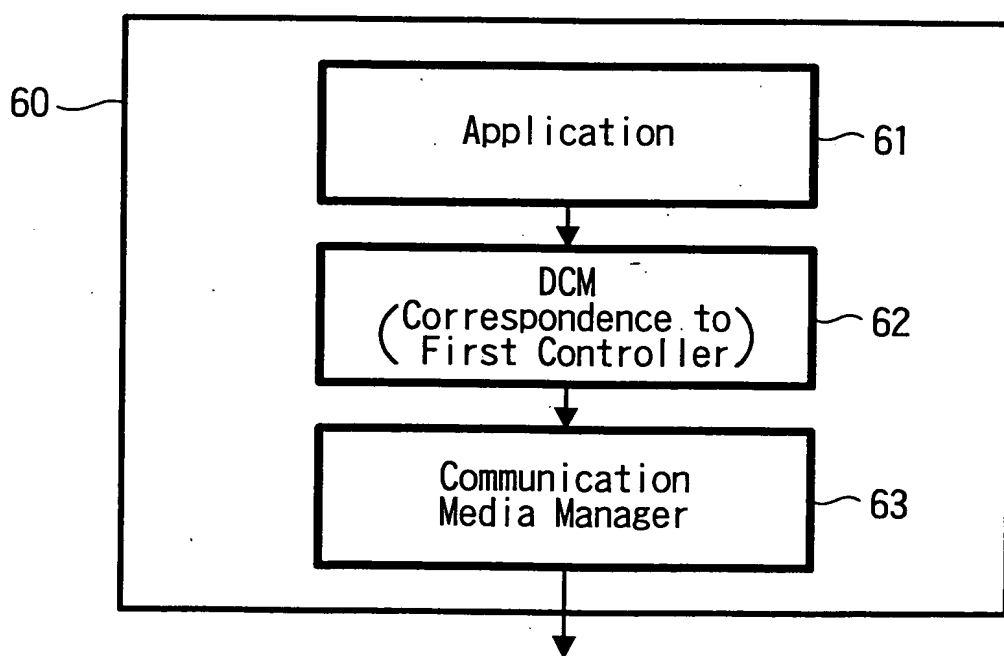
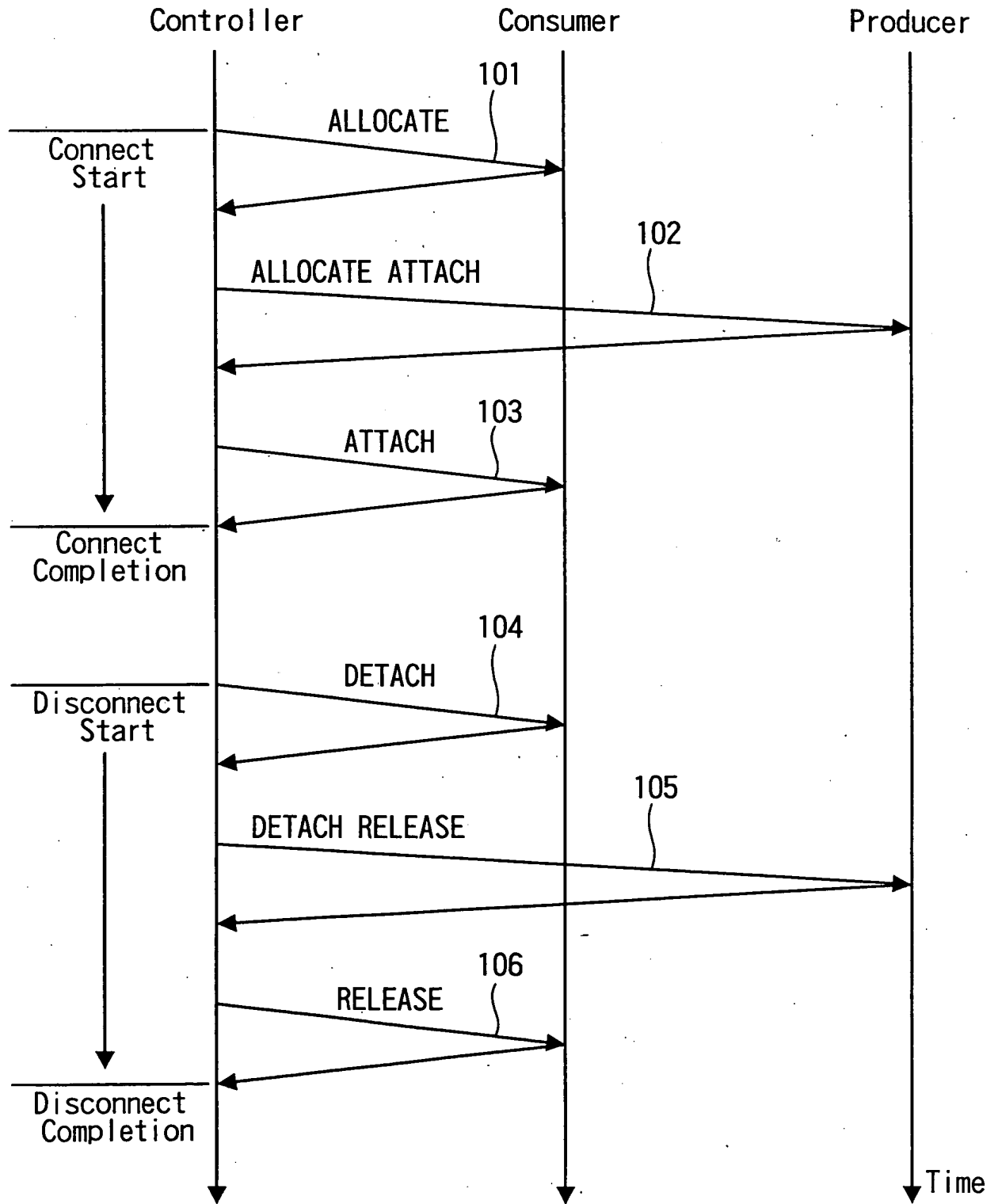


FIG. 11



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FIG. 12



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